



(2) **EUROPEAN PATENT APPLICATION**

(3) Application number: 93103101.7

(5) Int. Cl. 5: G09G 3/36

(2) Date of filing: 26.02.93

(2) Priority: 28.02.92, JP 7-5980/92
01.09.93 Bulletin 93/35

(4) Designated Contracting States: DE FR GB

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(6) Liquid crystal display.
(5) A liquid crystal display driven in multiplex with a plurality of pixels arranged in a matrix comprises a main display and at least one sub-display located at least partly around the periphery of the main display.

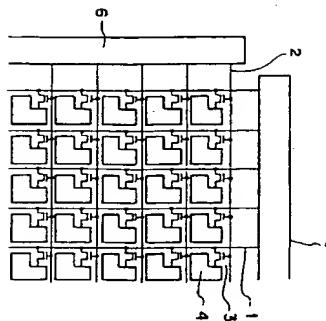


FIG. 1

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a liquid crystal display, and more particularly to a liquid crystal display which enables the optimal display.

Related Background Art

In recent years, the studies concerning liquid crystals have advanced, and liquid crystals have been developed as an image display medium for television by making use of the characteristics of liquid crystal. In particular, liquid crystal displays are expected for portable television or large-screen television because the thickness of screen can be reduced, and partially have been put on the market.

However, the liquid crystal display has several problems inherent to the properties of liquid crystal. One problem is that it has the non-uniform orientation on the edge of screen. This non-uniform orientation on the edge of screen is considered due to the fact that pixels on the edge of screen lack adjacent pixel electrodes, producing a step in the pixel electrode substrate, which step may influence the orientation of liquid crystal.

Also, even if this step is relieved, the liquid crystal pixels other than the pixels on the edge of screen, which can take arbitrary orientations, may be influenced by the drive voltage of pixel electrodes on the edge of screen, thereby having effects on a portion of the liquid crystal corresponding to the pixel electrodes on the edge, causing indistinct display thereon.

SUMMARY OF THE INVENTION

In the light of the above-described problems, it is an object of the present invention to provide a liquid crystal display which can make excellent high quality display over an entire area of screen by resolving problems such as indistinct image on the edge of screen in the liquid crystal display.

Also, it is another object of the present invention to provide a liquid crystal display which can overcome a lacking problem of display on the screen due to insufficient precision in the alignment with a light shielding layer around the periphery of the display. Moreover, it is another object of the invention to provide a liquid crystal display which allows for a

sub-display of character information apart from the display portion.

It is another object of the present invention to provide a liquid crystal display which is driven in multiplex, with a plurality of pixels arranged in a matrix, characterized by comprising a main display, and at least one sub-display located at least partly around the periphery of said main display.

Also, it is another object of the present invention to provide a liquid crystal display comprising:

a main display having a plurality of pixels arranged in a matrix, and a sub-display provided at least partly around the periphery of said main display, switch means for selecting between a first information signal displayed in said main display and a second information signal displayed in said sub-display, and a drive signal processing circuit for enabling said selected information to appear on said display. Additionally, it is another object of the present invention to provide a liquid crystal display comprising:

a main display having a plurality of pixels arranged in a matrix, and a sub-display provided at least partly around the periphery of said main display.

input means for inputting a first information signal displayed on said main display and a second information signal displayed on said sub-display,

storage means for storing said second information signal,

a first signal processing circuit for making signal processing corresponding to said main display based on said first information signal, and

a second signal processing circuit for making signal processing corresponding to said sub-display based on said second information signal.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view showing a first embodiment of the present invention.

Fig. 2 is a drive circuit diagram in the first embodiment of the present invention.

Figs. 3 to 8 are waveform charts which are applicable to the first embodiment of the invention.

Figs. 7, 19A and 19B are views showing a second embodiment of the invention.

Fig. 9 is a circuit diagram of the second embodiment of the invention.

Fig. 10 is a block diagram showing a fourth embodiment of the invention.

Fig. 11 is a block diagram showing a fourth embodiment of his invention.

Figs. 12 and 20 are a block diagram and a view showing a fifth embodiment of the invention, respectively.

Figs. 13 to 15 are diagrams showing the construction examples in the fifth embodiment of the invention.

Fig. 16 is a block diagram showing a sixth embodiment of the invention.

Fig. 17 is a block diagram showing a seventh embodiment of the invention.

Fig. 18 is a block diagram showing an eighth embodiment of the invention.

Fig. 19 is a block diagram showing a ninth embodiment of the invention.

Fig. 20 is a block diagram showing a tenth embodiment of the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

A liquid crystal display comprises a main display for displaying the image, and a sub-display provided at least partly around the periphery of the main display. In the present invention, the main display may be a conventional image display for displaying the image such as a normal high quality image or character. The sub-display is used for a portion not requiring very high image quality, such as character or black representation to improve the image quality on the main display. However, the sub-display can be also used for the image, if required.

In particular, the black representation in the sub-display is effective to raise the distinctness of the image by lightening the screen, and provided in at least one part of the top and bottom, and left and right of the main display, preferably, around the periphery of the main display. The black representation according to the present invention is durably AC driven to prevent the burning of liquid crystal. Further, it is possible that besides the main display and the sub-display with black representation, a second sub-display for displaying the character may be further provided around the periphery of the main display.

In the present invention, the main display and the sub-display may be driven synchronously, but can be driven asynchronously by using separate drive circuits.

The region of black representation according to the present invention is preferably wider to prevent degradation in the display characteristics on the screen, but the width of about five to six scan or display lines can offer sufficient effects, and yields no decrease in the precision of the alignment with a light shielding layer, as will be described later. Normally, ten or more lines allows the above degradation to be prevented almost completely.

In displaying characters, the number of scan lines making up at least one character may be satisfied, but in view of the above-mentioned degradation problem, substantially ten or more lines

are necessary.

The liquid crystal display of the present invention is also applicable to a color display. The color display has the color filters of three colors, i.e., green (G), red (R) and blue (B), provided on an opposed electrode substrate for each pixel to correspond in sequence to make a multi-color display with the coloration of those filters. In the present invention, when the black representation is made in the color display, it is preferable to also provide the color filter in the region of black representation.

In the present invention, in making the black representation, it is possible to further lighten the screen by providing a light shielding layer on the opposed electrode substrate corresponding to the black representation region. This light shielding layer can extend beyond the black representation region, wherein the display area except for the black representation may be defined by an opening portion of the light shielding layer. In this way, the alignment of the light shielding layer with the black representation region is facilitated, resulting in the improvement in the manufacturing yields.

In the present invention, the size and shape of and the interval between each pixel electrodes, are preferably equal in both the main display and the sub-display, and between both displays, although the size and shape and the interval are not necessarily equal. It is only necessary that the orientation of liquid crystal on the edge of the main display has the same characteristics of orientation as on the other portion than the edge of the main display so as to give rise to substantially uniform orientation.

Embodiments

The present invention will be specifically described below in connection with the embodiments, but is not limited to those embodiments.

Embodiment 1

Fig. 1 shows a schematic constitutional view of a first embodiment to which the present invention is applicable. A device of Fig. 1 is a liquid crystal display of the active matrix drive type in which a switching element consisting of a TFT (thin film transistor) is provided for each pixel. In the figure, 1 is a display (signal) line, 2 is a scan (signal) line, 3 is a TFT element, 4 is a pixel electrode, 5 is a horizontal shift register, and 6 is a vertical shift register.

Fig. 2 shows a drive circuit diagram of the liquid crystal display. In the figure, 7 is an image signal input terminal, 8 is a black level signal terminal, 9 is a Y/C signal processing circuit, 10 is a pulse generator, 11 is a chroma signal line, 12 is

a bright signal, 13 is a switching line, 14 is an analog switch, 15 is an RGB decoder (primary color signal converter), 16 is a drive signal processing circuit, 17 is an LCD with shift register (liquid crystal panel), 18 is a vertical blanking pulse signal line, and 19 is a horizontal blanking pulse signal line.

Figs. 3 and 4 are waveform charts of the circuit of Fig. 2 in Fig. 3, 31 is a bright signal waveform, 32 is a black level signal waveform, 33 is a horizontal blanking pulse, 34 is an analog switch output waveform, 35 is a reference voltage, and 36 and 37 are black level voltages. Normally, the potentials 36 and 37 have the same value. Also, Fig. 4 shows the behaviors of some signals near the vertical blanking period. In the figure, 41 is a first field bright signal, 42 is a second field bright signal, 43 is a vertical blanking pulse, 44 is a second field horizontal blanking pulse in the first field, 45 is a horizontal blanking pulse in the second field, 46 is an output waveform of the analog switch in the first field and 47 is an output waveform of the analog switch in the second field.

In Fig. 2, an image signal input via the image signal input terminal 7 is input into the Y/C signal processing circuit 9 on one hand, and the pulse signal generator 10 on the other hand. The image signal of the Y/C signal processing circuit 9 is separated into a bright signal and a chroma signal, which are output to signal lines 11, 12, respectively. The bright signals 31, 41, 42 input to the analog switch 14. The analog switch 14 is controlled by a signal on the switching line 13 to select a black level signal 32 to be input via the black level signal input terminal 8 when the signal is at "H" level, and the bright signals 31, 41, 42 when at "L" level. The signal on the switching line 13 is a logical sum of horizontal blanking signals 33, 44, 45 and a vertical blanking signal 43 which are generated by the pulse generator 10. Accordingly, the waveform output via the analog switch 14 is a black level signal in the vertical and horizontal blanking periods, and a bright signal in other periods, as represented in the waveform charts 34, 45. This signal and a chroma signal through the chroma signal line 11 are input to an RGB decoder 15 for the conversion into an RGB signal. Each primary color signal is input into the drive signal processing circuit 16 to make the signal processing for driving the LCD and then sent to the LCD 17.

The shift registers of the LCD 17, that is, a display line shift register (horizontal shift register 5 in Fig. 1) and a scan line shift register (vertical shift register 6 in Fig. 1), have more stages by the number of black level drive pixels, respectively, to output a portion to be driven by a black level signal around the periphery of the image display. The start pulse for such shift register is issued at an

earlier timing by the number of black level drive pixels, such as a waveform 51 of Fig. 5 in the case of display line, so that the range as indicated by 52 is sampled. Accordingly, the sub-display on the left and right hands of screen can be represented in black. Conventionally, the start pulse of the shift register occurred at a position 53, and the sampling range was as indicated by 54, so that the intended

Likewise, if the start pulse of the scan line shift register is made earlier by the number of scan lines to be scanned by the black level, such as a waveform 61 in Fig. 6, the sub-display on the top and bottom of the screen can be represented in black.

Embodiment 2

Fig. 7 shows a second embodiment of the present invention. This embodiment is a color display of the active matrix type using a TFT for the switching element. In the figure, 71 is a TFT substrate, 72 to 72" are insulation layers, 73 is a pixel electrode, 74 and 74" are orientation control members, 75 is a seal member, 76 is a liquid crystal, 77 is an opposed electrode, 78 is a color filter, 79 is a partition plate, 80 is a polarizing plate, 81 is a main display, and 82 is a sub-display for the black representation. As in this embodiment, by providing electrodes 74 and 74" as orientation control members, the switching of the image signal and the orientation of liquid crystal can be held uniform, resulting in improved distinctness of image on the edge of the main display 81.

Fig. 8 shows a circuit in this embodiment in which the switching of the image signal and the black level signal is made through an RGB signal, but not a bright signal. In the figure, 83 is an image signal input terminal, 84 is a black level signal input terminal, 85 is an RGB decoder, 86 is a pulse generator, 87 is an analog switch, 88 is a drive circuit, and 89 is an LCD panel.

Further, Fig. 9 shows the main display and sub-display areas, where a light shielding layer is further provided in this embodiment. In the figure, 91 is a main display, 92 is a light shielding layer, and 93 is a black representation region. In this figure, the light shielding layer 92 extends from the top to the bottom of the main display 91, and the light shielding layer 92 is located in the black representation region 93 inwardly of the main display, and can limit the sub-display.

In this embodiment, the black representation can be made by using the coloration in a color display, the arrangements appropriate for the main display, and the sub-display respectively, may be taken among several RGB pixel arrangements, but it is not necessary to put together both arrangements. Fig. 19a shows a device example in which the pixel arrangement of the main display is di-

ferent from that of the sub-display. In the figure, 191 is a main display, and 192 is a sub-display. In this way, by taking the optimal pixel arrangement for the display purposes, the image quality can be improved.

In this embodiment, it will be appreciated that the main display may display the image except for characters in color, and the sub-display may display character information in black and white. This device example is shown in FIG. 19B. In the figure, 191 is a main display, and 192 is a sub-display. The sub-display formed in black and white can have a greater definition than in color, allowing the character information to be read more clearly.

Embodiment 3

Fig. 10 shows a third embodiment of the present invention. This embodiment is a liquid crystal display of the dot matrix type. In FIG. 10, 191 is a black level signal input terminal, 192 is an image signal input terminal, 193 is pulse generator, 194 is a drive signal generator, 195 is a logic converter, 196 is a level shift, 197 is a scan side multiplexer, 198 is an LCD panel, 199 is a scan level generator, 200 is a signal side multiplexer, 201 is a level shift, and 202 is a signal level generator.

In this case, like the previous embodiments, a black level signal is supplied from the signal side multiplexer 190 to signal lines corresponding to both the left and right edges of the LCD panel 198. That is, in accordance with an image signal and with an image signal from the scan side multiplexer 197, the area of the LCD panel 198 except for the scan lines corresponding to both the upper and lower edges of the LCD panel 198 is scanned at certain timings.

That is, in accordance with an image signal corresponding to one scanning or one screen, either the black level signal or the image signal is supplied to the logic converter 195 by the pulse generator. And in accordance with a signal from the logic converter 195, the generation timing for the scan level signal and the signal level signal is taken. With this embodiment, it is possible to display an excellent image without degradation on the edges of the image.

Embodiment 4

Fig. 11 shows a fourth embodiment of the present invention. This embodiment is a device in which the sub-display displays the character information and the main display displays the image except for characters. In FIG. 11, 113 is an image signal input terminal, 114 is a Y/C signal processor, 115 is a pulse signal generator, 116 is a Control circuit, 117 is a ROM (read only memory),

118 is an external control signal input terminal, 119 is an RGB decoder, 120 is a drive circuit, and 121 is an LCD panel. In this embodiment, the kinds of characters to be displayed on the screen are controlled by an external control signal, so that corresponding data can be output from the ROM. With this circuit, the output state of no character, e.g., black representation, is enabled. This embodiment allows both the character information and the image to be displayed distinctly, because they are independently displayed from each other.

Embodiment 5

Fig. 12 shows a circuit diagram of a fifth embodiment according to the present invention. This embodiment allows the character information to be displayed in the sub-display, and the image except for the character information to be displayed in the main display. In this embodiment, the main display and the sub-display can be driven as synchronously, that is, independently of each other. In FIG. 12, 122 is an image signal input terminal, 123 is an external control signal input terminal, 124 is an image signal detector, 126 is a signal processing circuit, 127 is a ROM, 128 is a signal processing circuit, 129 and 130 are clock pulse generators, 131 is a drive circuit of the main display, 132 is a main display LCD, and 133 is a sub-display LCD.

Figs. 13 to 15 show combination examples of the main display and the sub-display in this embodiment. In the figure, 134 is a horizontal shift register for sub-display having a start pulse input terminal and transfer clock input terminals ϕ_{C1} and ϕ_{C2} , 135 is a display signal input terminal for sub-display, 136 is sample holder for sub-display, and 137 is a vertical shift register for sub-display having a start pulse ϕ_{C3} and transfer clock input terminals ϕ_{C4} and ϕ_{C5} , 138 is a vertical shift register for main display having a start pulse input terminal ϕ_{C6} and transfer clock input terminals ϕ_{C7} and ϕ_{C8} , 139 is a sample holder for main display, and 140 is a shift register for main display having a start pulse input terminal ϕ_{C9} and transfer clock input terminals ϕ_{C10} and ϕ_{C11} , 141 is a video signal (display signal for main display) input terminal.

Embodiment 6

Fig. 16 shows a sixth embodiment of the present invention. This embodiment has a circuit in which the embodiment 5, the display signal lines and the scan signal lines in the sub-display are short-circuited to one or more transmission lines, without intervention of any shift register, so that each pixel is driven with the same signal. In FIG. 16, 161 is an image signal input terminal, 162 is an image signal detector, 163 is a sub-display drive signal generator, 164 is a signal processing circuit, 165 is a clock pulse generator, 166 is a drive circuit, 167 is a control signal (e.g., clock) signal generator, 168 is a main display LCD, and 169 is a sub-display LCD. Fig. 18 shows a device example in which the scan line side and the display line side are both short-circuited in this embodiment.

In this embodiment, nothing the sub-display plays in making the black representation may be driven in asynchronism with the main display, and displays no image, unlike the main display, the signal is directly input into the signal line without any circuit such as a shift register and a sample holder. Accordingly, the sub-display for black representation can be implemented without the use of any large circuit.

Embodiment 7

Fig. 17 shows a circuit of a seventh embodiment according to the present invention. This embodiment is a device for displaying the image such as a still image in which the sub-display is subdivided into a plurality of dividers. In the figure, 171 is an image signal input terminal, 172 is an

external control signal input terminal, 173 is an image signal detector, 174 is a control circuit, 175 is a signal processing circuit, 176 is a frame memory, 177 and 178 are clock pulse generators, 179 is a drive circuit, 180 is a signal processing circuit, 181 is a main display LCD and 182 is a sub-display LCD.

In this embodiment, in place of a ROM used for storing the character as previously described, the frame memory 176 for storing image information is used, wherein the information in the frame memory 176 is output to the sub-display. Note that in this embodiment, by providing the sub-display LCD 182 around the periphery of the main display LCD 181, the main display LCD 181 can make more excellent display on the edge thereof. Also like the previous embodiments, the sub-display LCD is supplied with the black information, except when otherwise required, so that it is possible to further tighten the main display. The liquid crystal display of the invention allows the uniform and distinct image to be displayed on the entire area of screen including the edges thereof by providing a sub-display.

Also, by allowing the sub-display to display character information, separately from the main display, the main display and the sub-display are easier to view, resulting in high qualities of display. According to the present invention, there is provided a device for allowing the character information and others to be displayed in the optimal condition, as with the teletext in the television broadcasting.

Note that the shape of pixel in the sub-display may be different from that in the main display, either in the dot matrix or the segment form. While the present invention has been described, it will be understood that various changes and combinations may be made within the scope of the present invention.

A liquid crystal display driven in multiplex, with a plurality of pixels arranged in a matrix comprises a main display, and at least one sub-display located at least partly around the periphery of said main display. 2. The liquid crystal display according to claim 1, characterized in that said sub-display always makes black representation.

Claims

1. A liquid crystal display which is driven in multiplex with a plurality of pixels arranged in a matrix, characterized by comprising a main display, and at least one sub-display located at least partly around the periphery of said main display.

2. The liquid crystal display according to claim 1, characterized in that said sub-display always makes black representation.

3. The liquid crystal display according to claim 1, characterized in that said sub-display displays character information.

4. The liquid crystal display according to claim 1, characterized in that said main display and said sub-display are synchronously driven.

5. The liquid crystal display according to claim 1, characterized in that said main display and said sub-display are asynchronously driven.

6. The liquid crystal display according to claim 1, characterized by comprising a sub-display driven synchronously with said main display and a sub-display driven asynchronously therewith.

7. The liquid crystal display according to claim 5, characterized in that said sub-display driven synchronously with said main display is always held in black representation and said sub-display driven asynchronously displays the character information.

8. The liquid crystal display according to claim 2, characterized in that a light shielding layer is provided on a region of an opposed electrode substrate at least corresponding to the black representation of said sub-display.

9. The liquid crystal display according to claim 8, characterized in that said main display and said sub-display not making black representation are defined by an opening portion of said light shielding layer provided on said opposed electrode substrate.

10. The liquid crystal display according to claim 1, characterized in that said opposed electrode substrate has color filters corresponding to pixels.

11. A liquid crystal display comprising:
a main display having a plurality of pixels arranged in a matrix, and a sub-display provided at least partly around the periphery of said main display;
switch means for selecting between a first information signal displayed in said main display and a second information signal displayed in said sub-display; and
a drive signal processing circuit for enabling said selected information to appear on said display.

12. The liquid crystal display according to claim 11, comprising signal generating means for switching said switch means.

13. The liquid crystal display according to claim 11, wherein said switch means is switched corresponding to said first information signal.

14. The liquid crystal display according to claim 12, wherein said signal generating means has a pulse generator for generating a horizontal blanking pulse signal and a vertical blanking pulse signal based on said first information signal, and a logical sum circuit for those signals.

15. The liquid crystal display according to claim 13, wherein said first information signal has a bright signal.

16. The liquid crystal display according to claim 13, wherein said first information signal has an RGB signal.

17. The liquid crystal display according to claim 11, wherein said main display and said sub-display have different pixel arrangements.

18. The liquid crystal display according to claim 11, wherein said main display and said sub-display have different pixel shapes.

19. The liquid crystal display according to claim 18, wherein the pixel shape of said sub-display is a segment.

20. The liquid crystal display according to claim 11, wherein said main display is provided with filters corresponding to pixels.

21. The liquid crystal display according to claim 20, wherein said filter is a filter for color display.

22. The liquid crystal display according to claim 11, wherein said main display and said sub-display are correspondingly provided with filters.

23. The liquid crystal display according to claim 22, wherein said filter is constituted of color filters of R, G, B.

24. The liquid crystal display according to claim 11, wherein said main display and said sub-display each have filters corresponding to color filters of R, G, B, and have different filter arrangements.

25. The liquid crystal display according to claim 11, wherein said second information signal is a black level signal.

26. The liquid crystal display according to claim 11, wherein said second information signal is a character information signal.

27. The liquid crystal display according to claim 11, wherein said second information signal is an image signal different from said first information signal.

28. A liquid crystal display comprising:
a main display having a plurality of pixels arranged in a matrix, and a sub-display provided at least partly around the periphery of said main display;
input means for inputting a first information signal displayed on said main display and a second information signal displayed on said sub-display;
storage means for storing said second information signal;
a first signal processing circuit for making the signal processing corresponding to said main display based on said first information signal; and
a second signal processing circuit for making the signal processing corresponding to said sub-display based on said second information signal.

29. The liquid crystal display according to claim 28, wherein said second information signal is character information.

30. The liquid crystal display according to claim 28, wherein said second information signal is image information.

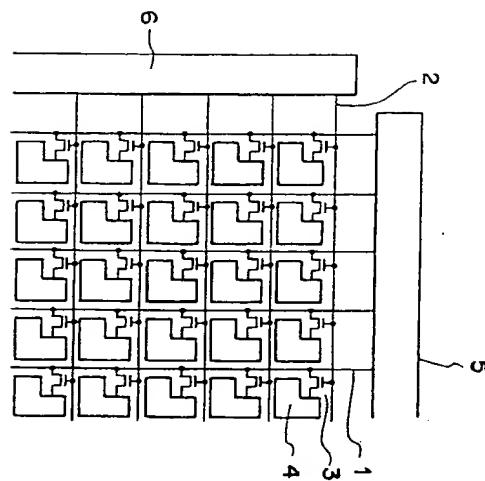
31. The liquid crystal display according to claim 28, wherein said storage means is a frame memory.

32. The liquid crystal display according to claim 28, wherein said storage means is a random access memory.

33. The liquid crystal display according to claim 28, wherein said main display and said sub-display are operated asynchronously.

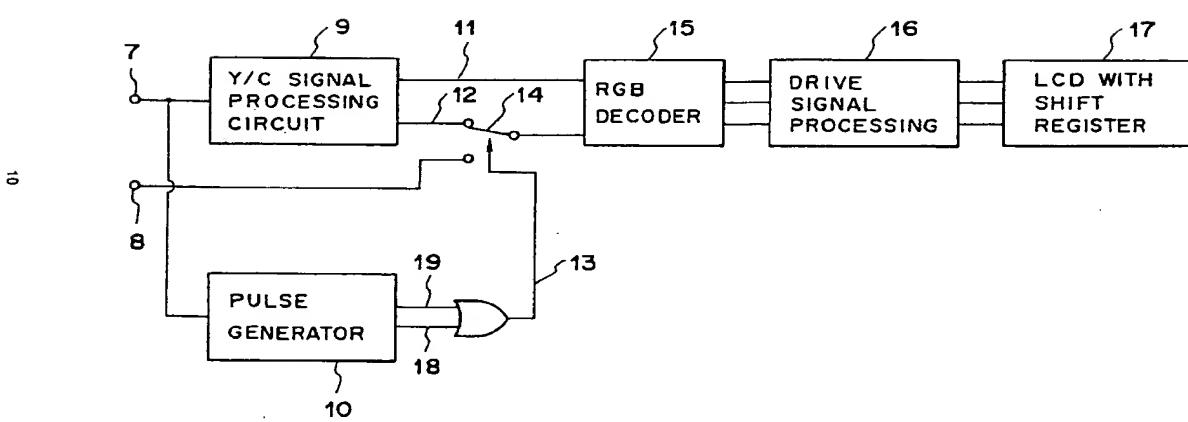
34. The liquid crystal display according to claim 28, wherein said main display and said sub-display are operated synchronously.

FIG. 1



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FIG. 2



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FIG. 3

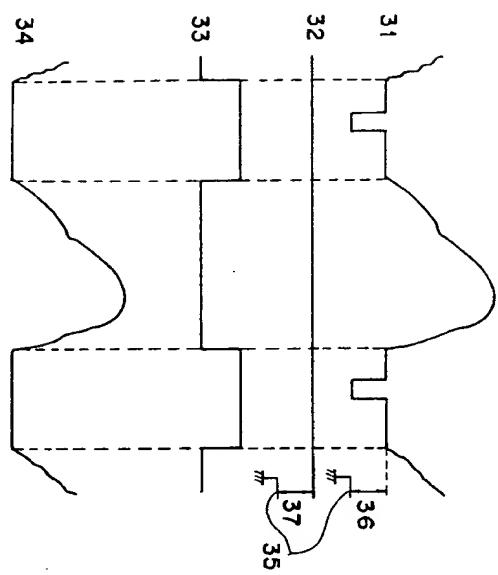


FIG. 4

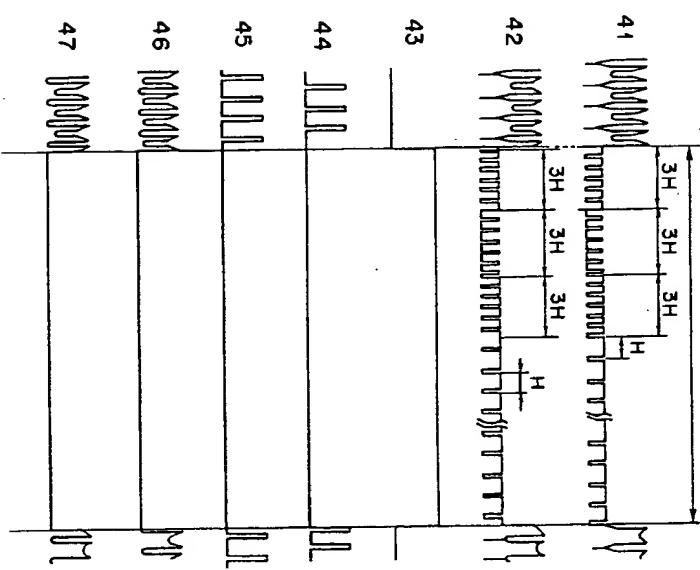
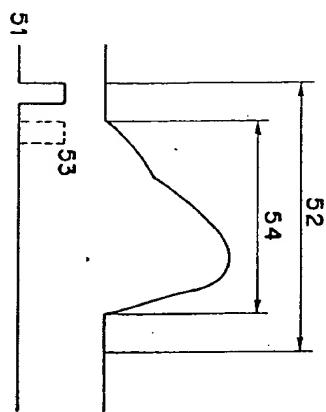
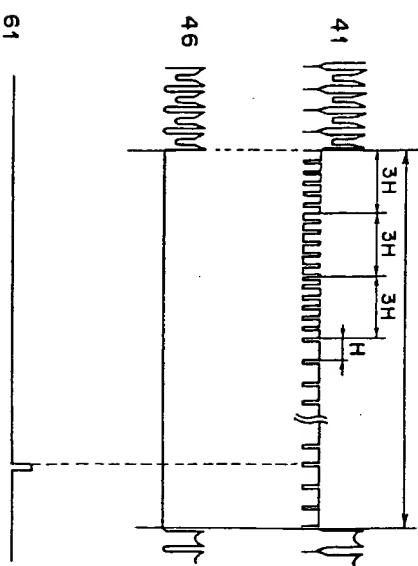


FIG. 5



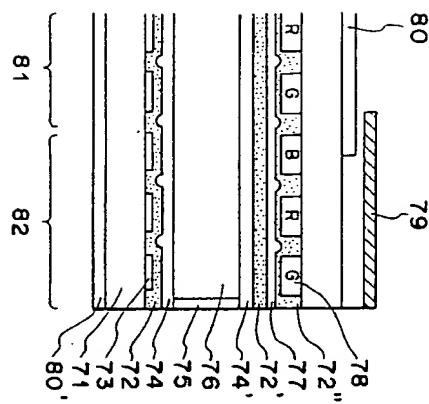
51, 53 DISPLAY LINE SIDE SHIFT
REGISTER START PULSE
52, 54 SAMPLING REGION

FIG. 6



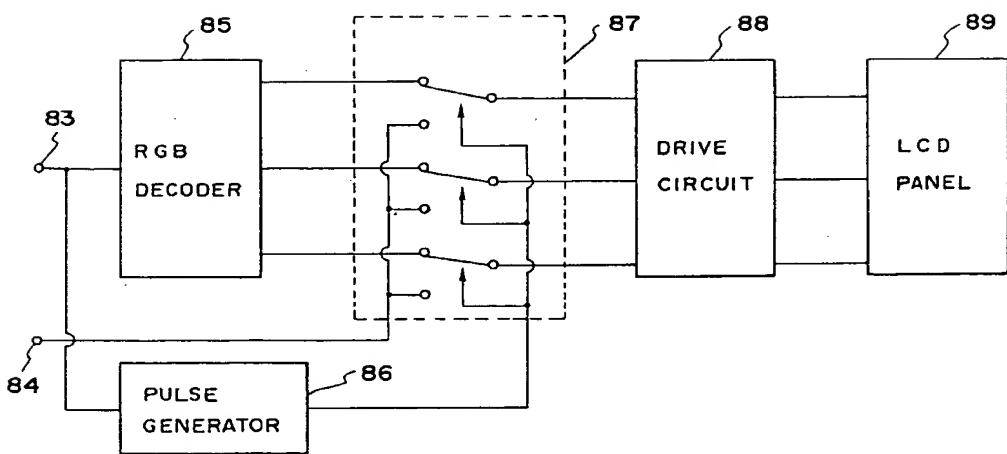
61... SCANNING LINE SIDE SHIFT
REGISTER START PULSE

FIG. 7



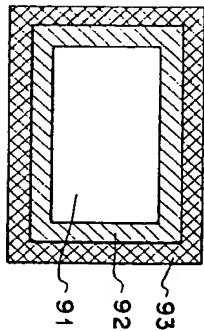
15

FIG. 8



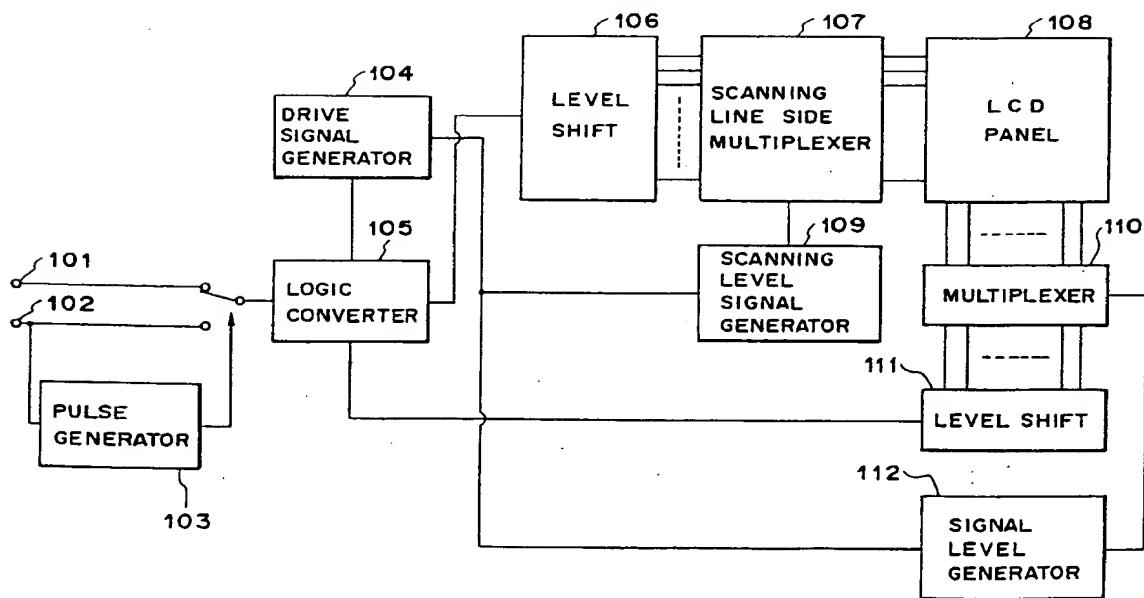
16

FIG. 9



17

FIG. 10



18

FIG.11

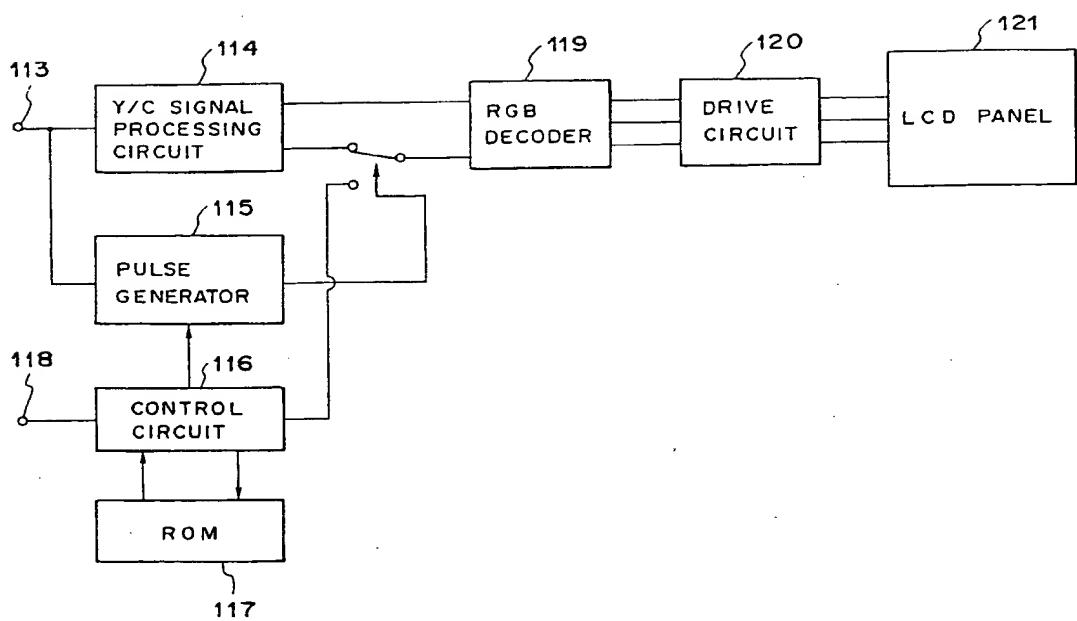


FIG.12

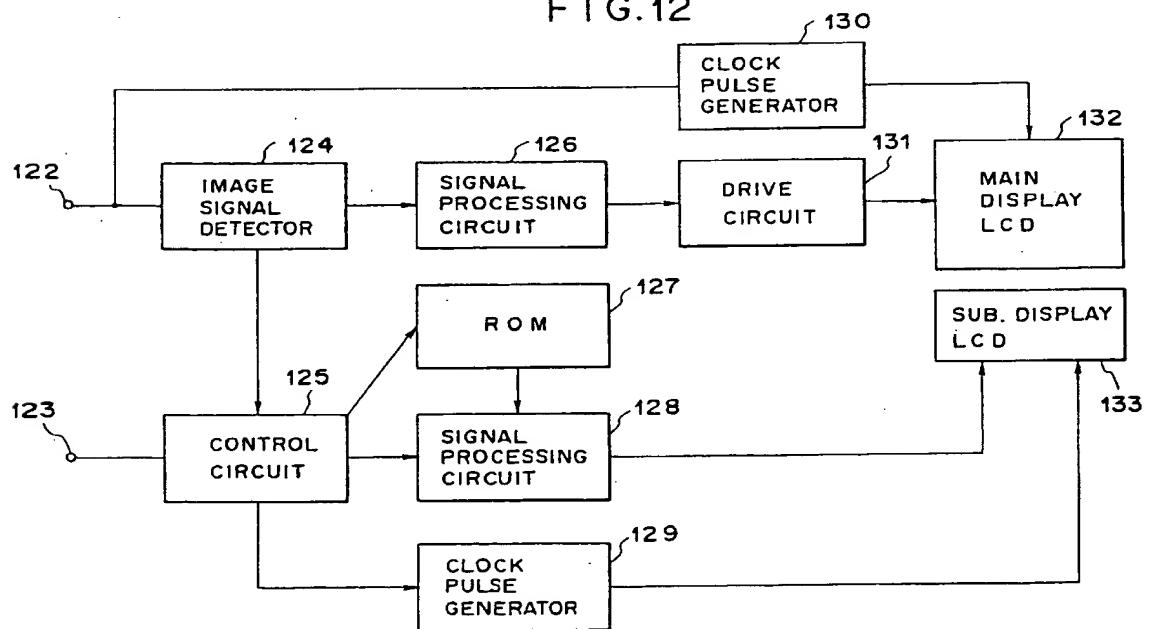
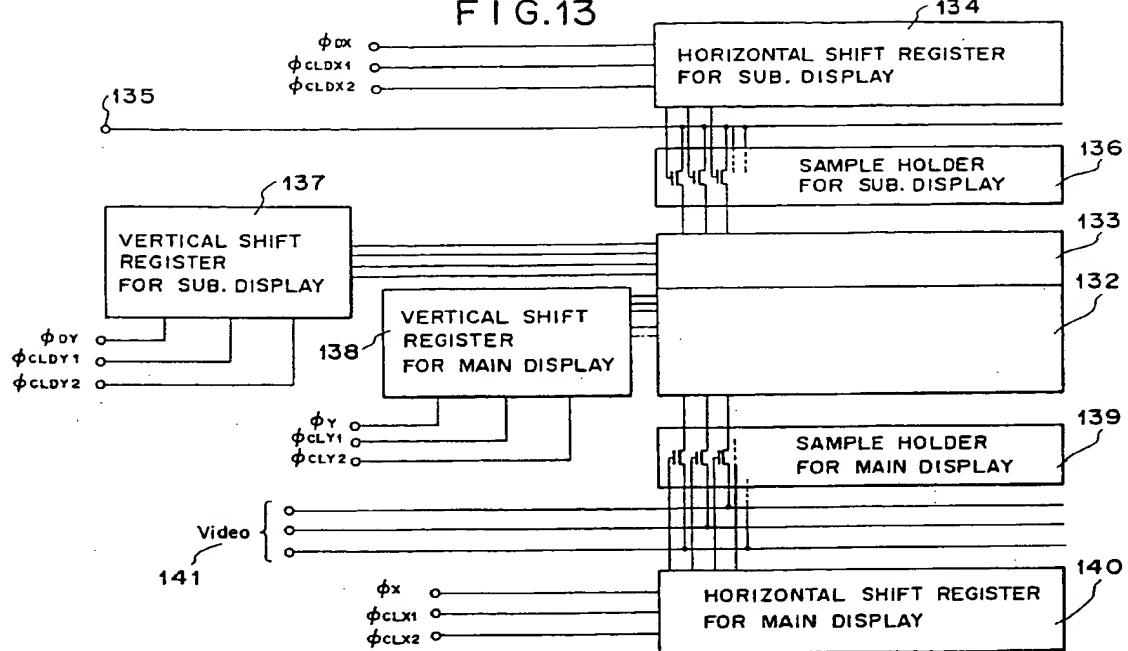


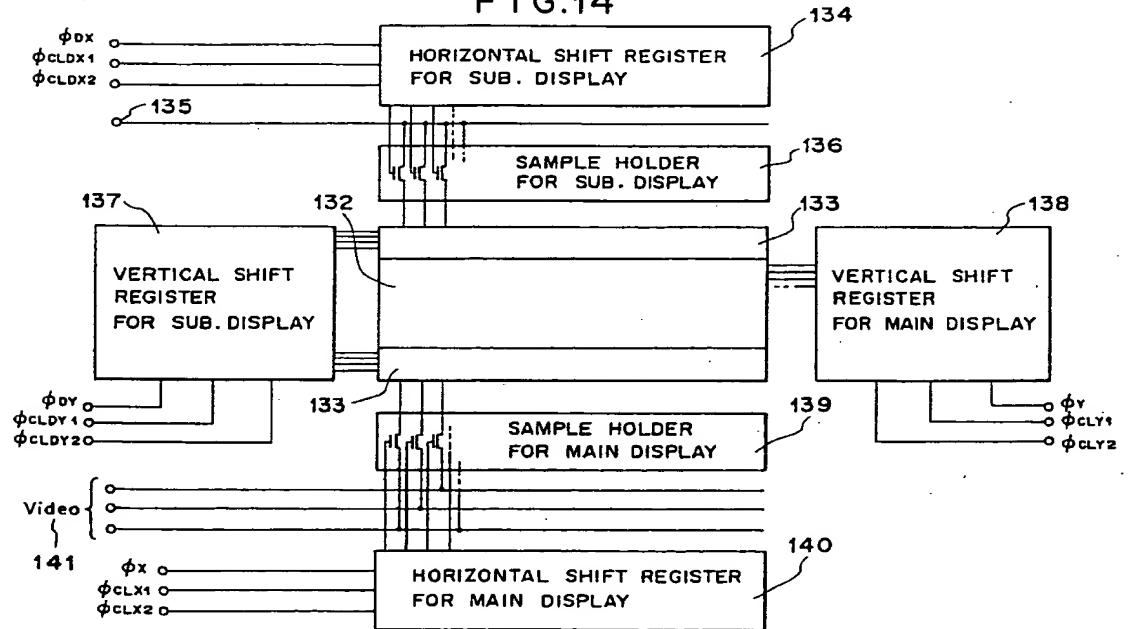
FIG.13



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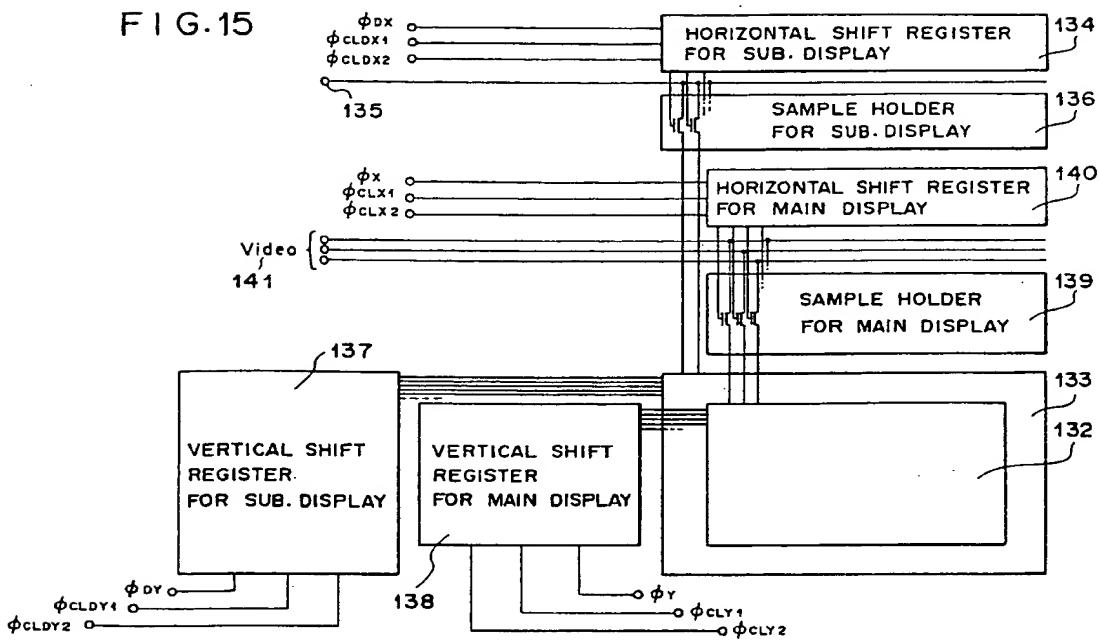
FIG.14



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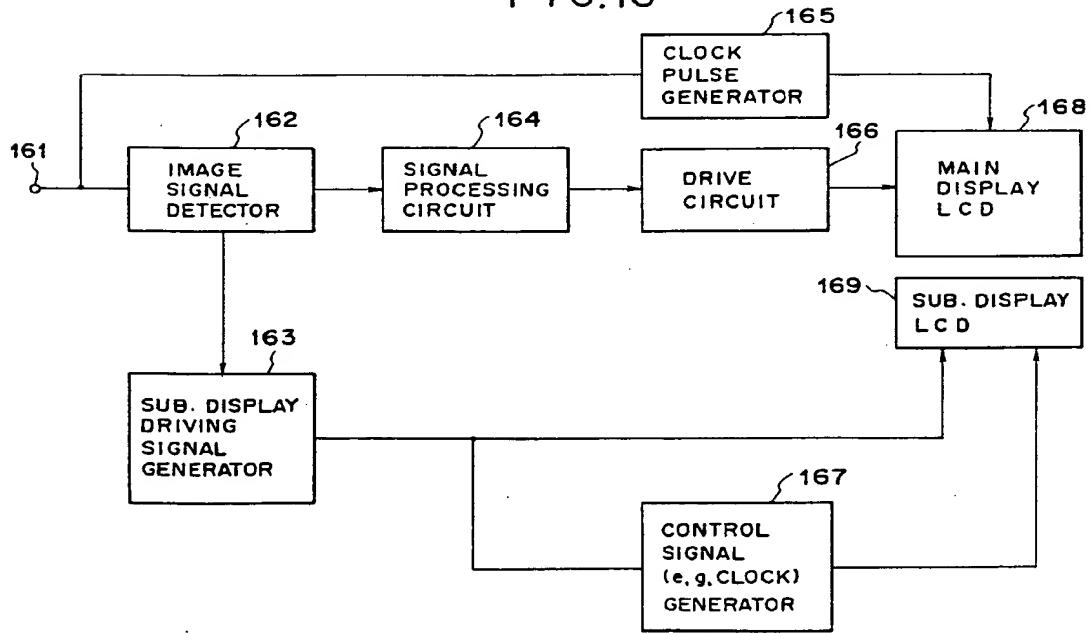
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FIG. 15



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FIG. 16



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FIG.17

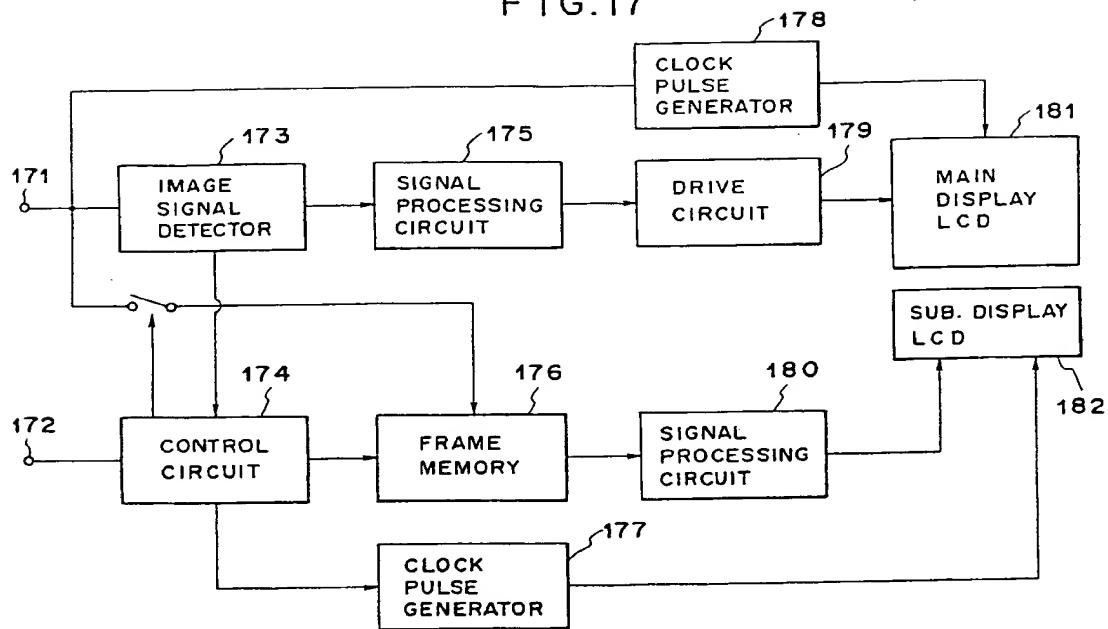


FIG.18

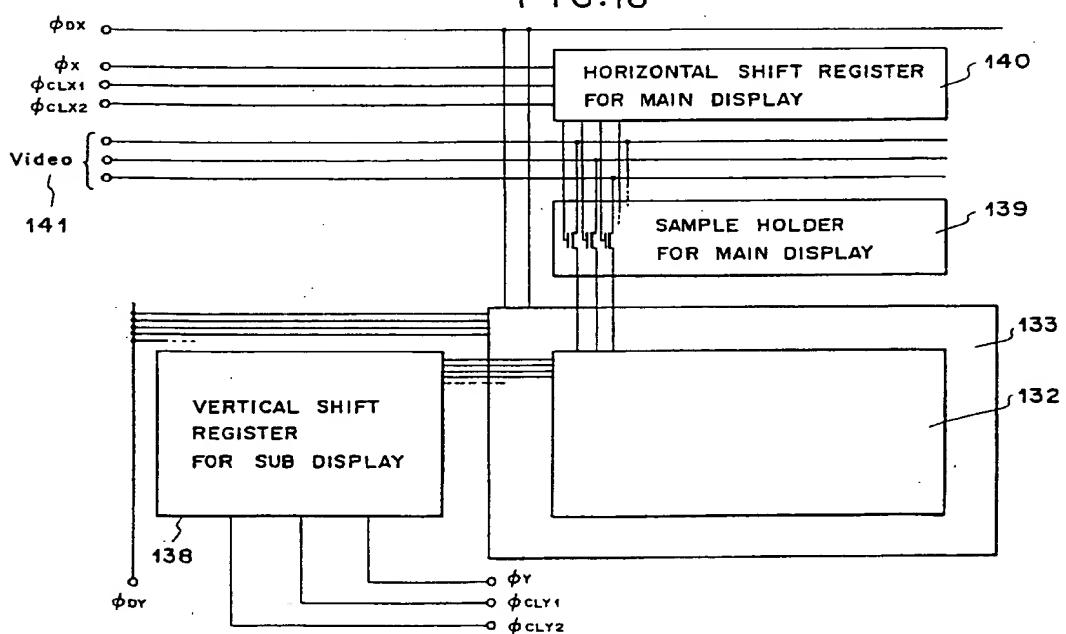


FIG.19A

R	B	G	R	B	C	R	
B	G	R	B	G	R	B	G
G	R	B	R	B	G	R	
R	B	G	R	B	G	R	B
B	G	R	B	G	R	B	G
G	R	B	G	R	B	G	R

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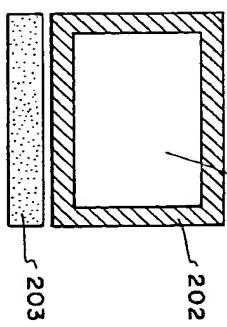
FIG.19B

R	B	G	R	
B	G	R	B	G
R	B	G	R	
G	R	B	R	
R	B	G	R	

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FIG.20



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European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT		EP 93103101.7	
Category	Character of document with indication, where appropriate, of relevant pages	Relevant to claim	Classification of the application (as of 01.01.93)
X	DE - A - 3 930 581 (ASEA BROWN BOVERI) * Abstract *	1-3	G 09 G 3/36
X	GB - A - 2 112 184 (MITSUBISHI) * Abstract *	1	
X	GB - A - 2 157 047 (JONES) * Abstract *	1	

TECHNICAL FIELDS SEARCHED (as of 01.01.93)	
G 09 G 3/00 G 02 F 1/00	

The present search report has been drawn up for all claims.		
Date of search	Date of completion of the search	Examiner
VIENNA	28-05-1993	KUNZ

CATEGORY OF CITED DOCUMENTS	
<input checked="" type="checkbox"/> A particularly relevant document <input checked="" type="checkbox"/> B document which is closely concerned with another document of the same category <input type="checkbox"/> C technological background <input type="checkbox"/> D administrative document <input type="checkbox"/> E technical document, not published, or <input type="checkbox"/> F document filed in the application <input type="checkbox"/> G document cited for other reasons <input type="checkbox"/> H member of the same patent family, corresponding <input type="checkbox"/> I informative document	

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